Electronic Pa							
Application Number:	100	10629302					
Filing Date:	28-	28-Jul-2003					
Title of invention:		TRANSCEIVER MODULE AND INTEGRATED CIRCUIT WITH DUAL EYE OPENER AND INTEGRATED LOOPBACK AND BIT ERROR RATE TESTING					
First Named Inventor/Applicant Name:	Lev	Lewis B. Aronson					
Filer:	Eric	Eric L. Maschoff/Kelli Clark					
Attorney Docket Number:	15-	15436.247.2.1.4					
Filed as Large Entity							
Utility under 35 USC 111(a) Filing Fees							
Description		Fee Code	Quantity	Amount	Sub-Total in USD(\$)		
Basic Filing:							
Pages:							
Claims:							
Miscellaneous-Filing:							
Petition:							
Patent-Appeals-and-Interference:							
Post-Allowance-and-Post-Issuance:							

Publ. Fee- early, voluntary, or normal

Description	Fee Code	Quantity	Amount	Sub-Total in USD(\$)
Extension-of-Time:				
Miscellaneous:				
	Total in USD (\$)			1810